



Electronic Filing System (EFS) Data
Electronic Patent Application Submission
USPTO Use Only

EFS ID: 60400
Application ID: 09880734
Title of Invention: Embedded processor with
watchdog timer for
programmable logic
First Named Inventor: Andrew Crosland
Domestic/Foreign Application: Domestic Application
Filing Date: 2001-06-12
Effective Receipt Date: 2004-05-05
Submission Type: Information Disclosure
Statement
Filing Type:
Confirmation number: 4950
Attorney Docket Number: 015114-053500US



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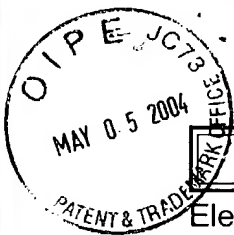
MAY 10 2004

Technology Center 2100

Total Fees Authorized:

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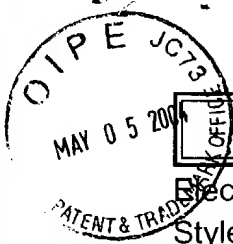
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Electronic Version v1.1

Stylesheet Version v1.1.0


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<table border="1"><thead><tr><th>Submitted by:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>Melvin D. Chan Registered Number: 39626</td><td>Melvin D. Chan</td><td>Attorney</td></tr></tbody></table>			Submitted by:	Elec. Sign.	Sign. Capacity	Melvin D. Chan Registered Number: 39626	Melvin D. Chan	Attorney		
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Comments										

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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
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US Patent Documents																																	
Note: Applicant is not required to submit a paper copy of cited US Patent Documents																																	
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